



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

**Tam et al.**

Appl. No.: 10/026,470

Filed: December 27, 2001

For: **A Thick Oxide P-Gate NMOS  
Capacitor for Use in a Phase-  
Locked Loop Circuit and Method  
of Making Same**

Confirmation No.: 7771

Art Unit: 2825

Examiner: Malsawma, L.H.

Atty. Docket: 1875.1350000

**Notice of Appeal From the Examiner to the Board  
of Patent Appeals and Interferences - Large Entity**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

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Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the final decision of the Examiner dated **August 7, 2003**, in which claims 1-19 were finally or twice rejected.

The fee (for a large entity) for filing a Notice of Appeal from the Examiner to the Board of Patent Appeals and Interferences (37 C.F.R. § 1.17(b)), along with any necessary extension fees (37 C.F.R. § 1.17(a)), is included in our PTO-2038 Credit Card Payment Form. In the event that extensions of time under 37 C.F.R. § 1.136 other than those otherwise provided for herewith are required to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

11/06/2003 RHARIS1 00000022 10026470  
01 FC:1401 330.00 OP

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

*Donald J. Featherstone*

Donald J. Featherstone  
Attorney for Applicants  
Registration No. 33,876

Date 11/15/03

1100 New York Avenue, N.W.  
Washington, D.C. 20005-3934  
(202) 371-2600